

**REMARKS/ARGUMENTS**

Claims 1-8 are pending. Claims 7 and 8 are allowed.

Claims 1 and 4 are amended to clarify that the bus architecture is an SMBus/I2C-compliant bus architecture.

Claims 2 and 5 are canceled.

Claims 3 and 6 depend from amended claims 1 and 4, respectively, and accordingly incorporate the SMBus/I2C bus architecture.

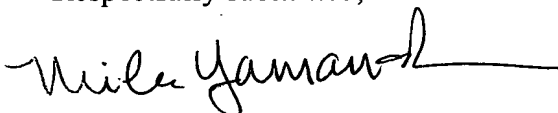
The Office Action indicated that dependent claims 2 and 5 would be allowable on the basis of the communication interface SMBus/I2C bus architecture. Therefore, the clarification that the bus architecture of claims 1 and 3 is a SMBus/I2C bus architecture is understood to overcome the rejection under 35 U.S.C. § 102. Similarly, the clarification that the bus architecture of claims 4 and 6 is a SMBus/I2C bus architecture is understood to overcome the rejection under 35 U.S.C. § 103. Accordingly, claims 1, 3, 4 and 6 are allowable.

In view of the forgoing amendment and remarks, Applicant submits that the present application is in condition for allowance. A Notice of Allowance is therefore respectfully requested.

The Commissioner is hereby authorized to charge payment of any fees required associated with this communication or credit any overpayment to Deposit Account No. 50-0337. If an extension of time is required, please consider this a petition therefor and charge any additional fees which may be required to Deposit Account No. 50-0337. A duplicate copy of this paper is enclosed.

June 8, 2005

Respectfully submitted,



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